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Figure 6.13 Illustration of capacitance per unit length.. Note that  $Q$  is expressed in coulomb/meter. Now recall from Fig. 6.11(a) that the channel voltage varies along the length of the transistor, and the charge density falls as we go from the source to the drain.

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Professor Razavi is a Fellow of IEEE, has served as an IEEE Distinguished Lecturer, and is the author of Principles of Data Conversion System Design, RF Microelectron- ics (translated to Chinese, Japanese, and Korean), Design of Analog CMOS Integrated Circuits (translated to Chinese, Japanese, and Korean), Design of Integrated Circuits for Optical Communications, and Fundamentals of Microelectronics (translated to Korean and Portuguese).

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456. Chap. 9. Cascode Stages and Current Mirrors.  $V_{CC}$ .  $Q_4$ .  $V_{b2}$   $Q_3$ .  $v_{out}$ .  $V_{b1}$   $Q_2$ .  $v_{in}$   $Q_1$ . Figure 9.57. unchanged. 31. Repeat Problem 30 if the lengths of both transistors are increased by a factor of  $N$  while the transistor widths and bias currents remain unchanged.. 32.

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